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CONFIRMATION NO. 9795

SERIAL NUMBER 10/561,552	FILING or 371(c) DATE 03/20/2006 RULE	CLASS 257	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 19415-005US1 PCT-04R-155/		
APPLICANTS Hidetoshi Nishikawa, Kyoto, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP04/09059 06/21/2004 ** FOREIGN APPLICATIONS ***** JAPAN 2003-177412 06/23/2003 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/10/2006						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/VIBOL TAN/</u> Examiner's Signature		<input checked="" type="checkbox"/> Met after Allowance VTan Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 1	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 1
ADDRESS FISH & RICHARDSON P.C. P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022 UNITED STATES						
TITLE Semiconductor integrated circuit device						
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